ABSTRACT OF THE DISCLOSURE

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A solid-state imaging device includes: an imaging region in which a plurality of pixels are arranged; and a signal line through which a signal of the imaging region is read out. An adding circuit for adding pixel signals obtained from two or more of the pixels is provided so that an output signal of the adding circuit is read out to the signal line. On the basis of a predetermined reference quantity of light incident onto the imaging region, a gain of the adding circuit in a condition in which a quantity of the incident light is above the reference quantity is controlled to be smaller than a gain of the adding circuit in a condition in which a quantity of the incident light is below the reference quantity. High sensitivity can be obtained with an adding circuit, and saturation of the adding circuit in large quantity of light can be suppressed, obtaining a wide dynamic range.